



CSMSS
CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S



CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 002 (M.S)

Email : shahuengg@gmail.com, principal@csmsseengg.org Website : www.csmsseengg.org

Department of Electronics Engineering (VLSI Design and Technology)

REPORT on –

**“Teachers Day Celebration at Department of Electronics Engineering
(VLSI Design & Technology)”**

Venue: Seminar Hall, 2nd Floor, A-Building, CSMSS CSCOE.

Date: 5th Sept. 2026

Time:- 11.30 AM to 12.30 PM

Goal:

The goal of the Teacher's Day Celebration was to express gratitude and appreciation towards the teachers for their valuable guidance, dedication, and contribution to students' academic and personal development. The event aimed to strengthen the bond between teachers and students through meaningful and engaging activities.

Objectives of the Event :

1. To express gratitude towards teachers for their continuous guidance, support, and mentorship.
2. To recognize and appreciate the dedication, efforts, and contributions of the department faculty.
3. To strengthen teacher-student relationships by creating a positive and joyful interaction beyond the classroom.
4. To provide a platform for students to express their respect, thoughts, and feelings towards their teachers.
5. To develop students' organizational and communication skills through their active participation in planning and conducting the celebration.

Learning Outcomes :

1. Enhanced teacher-student bonding through meaningful interaction and appreciation.
2. Improved communication skills of students through speeches, presentations, and participation.
3. Developed leadership and organizational skills among students involved in planning and conducting the event.
4. Encouraged creativity and teamwork through interactive activities such as teacher recognition awards and the Teacher's Day Wall.
5. Created a positive and respectful academic environment by promoting gratitude and appreciation



CSMSS
CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S



CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)

Email : shahuengg@gmail.com, principal@csmsseengg.org Website : www.csmsseengg.org

Department of Electronics Engineering (VLSI Design and Technology) towards teachers.

Benefits to Students:

1. Improved confidence and public speaking skills through active participation in the programme.
2. Developed teamwork and event-management skills by working together to organize and execute the celebration.
3. Strengthened interpersonal relationships with teachers and fellow students, creating a more supportive learning environment.

About the Event:

The **Students Association of VLSI Engineers (SAVE)** of the **Department of Electronics Engineering (VLSI D&T)**, CSMSS Chhatrapati Shahu College of Engineering, organized a **Teachers Day Celebration** on **5th September 2026**, from **11:30 AM to 12:30 PM** at the Seminar Hall, 2nd Floor, A-Building, CSMSS CSCOE.

The celebration commenced with **Saraswati Pujan** in the presence of all the faculty members of the department, including Respected **Head of Department, Dr. S.J. Honade Sir** and all department students. The programme then began with a soulful **Ganesh Vandana**, presented by two female students from the 2nd year. Following this, the students expressed their respect and gratitude towards all the teachers by **felicitating them with flowers**. Students from the **2nd year, 3rd year, and final year (B.Tech)** then **delivered individual speeches** highlighting the importance of teachers and their invaluable role in shaping students careers and lives.

The **Head of Department, Dr. S.J. Honade Sir**, addressed the gathering and shared his thoughts and wishes on the occasion. **Prof. T.A. Mohije Sir** also delivered an inspiring speech and conveyed his appreciation to the students for organizing the celebration. An engaging and entertaining **Teacher Recognition Activity** was then conducted. Students gave creative titles/awards to teachers based on their personalities and qualities. The respective teachers were presented with badges, making the activity enjoyable and memorable for everyone.

The **"Teachers Day Wall"** was subsequently introduced to the teachers and students. The wall provided an opportunity for everyone to write a few words or messages expressing their thoughts, gratitude, and wishes for their teachers. The programme concluded with a **Vote of Thanks**, expressing sincere gratitude to the Head of Department, faculty members, students, and all those who contributed to making the celebration successful.

Overall, the Teachers Day Celebration was a warm, engaging, and memorable event, successfully reflecting the respect and gratitude of the students towards their teachers.



CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

CHH. SHAHU COLLEGE OF ENGINEERING



Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 002 (M.S)

Email : shahuengg@gmail.com, principal@csmsseengg.org Website : www.csmsseengg.org

Department of Electronics Engineering (VLSI Design and Technology)
Flyer for the Event

 **STUDENTS ASSOCIATION OF VLSI ENGINEERS (SAVE)** 

*Cordially invites
all Teachers & Students of -*

Dept. of Electronics Engineering (VLSI D&T)

to the

**Teacher's Day
Celebration**



DATE : 5th Sept. 2026 (Saturday)
TIME : 11.30 AM to 12.30 PM
**VENUE : Seminar Hall, 2nd Floor, A-Building,
CSMSS CSCOE**





CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

CHH. SHAHU COLLEGE OF ENGINEERING

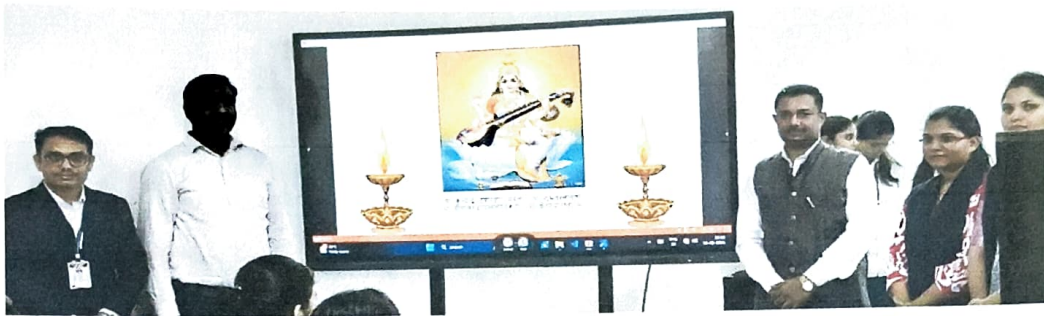
Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)

Email : shahuengg@gmail.com, principal@csmsseengg.org Website : www.csmsseengg.org



Department of Electronics Engineering (VLSI Design and Technology)

Glimpses of the Event





CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

CHH. SHAHU COLLEGE OF ENGINEERING



Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)

Email : shahuengg@gmail.com, principal@csmsseengg.org Website : www.csmsseengg.org

Department of Electronics Engineering (VLSI Design and Technology)



Mrs. K.B. Dandge
Event Coordinator

Dr. S. J. Honade.
HoD, EE (VLSI D&T)

Dr. G. B. Dongre
Principal, CSMSS CSCoE